

SESHADRI RAO GUDLAVALLERU ENGINEERING COLLEGE

(An Autonomous Institute with Permanent Affiliation to JNTUK, Kakinada)

Seshadri Rao Knowledge Village, Gudlavalleru

M.Tech I Semester (R25) First Mid Term Examinations

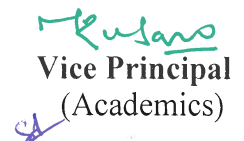
TIME TABLE

Timings: 10.00 a.m. to 12.00 Noon

03rd January 2026

Date	Structural Engineering	Power Electronics & Electric Drives	VLSI Design & Embedded Systems	CSE (Artificial Intelligence & Machine Learning)
05.01.2026 (Monday)	Theory of Elasticity	Renewable Energy Technology	Advanced Digital Systems Design	Mathematics for Machine Learning
06.01.2026 (Tuesday)	Structural Dynamics	Industrial Control Electronics	Embedded Hardware Platforms and Programming	Artificial Intelligence
07.01.2026 (Wednesday)	Matrix Analysis of Structures	Electrical Machine Modeling and Analysis	FPGA Design	Data Science Applications
08.01.2026 (Thursday)	Analytical & Numerical Methods for Structural Engineering	Power Electronic Converters	VLSI System Design	Advanced Data Mining
09.01.2026 (Friday)	Repair and Rehabilitation of Structures	Electric Vehicles	System on Programming Chip Design	Recommender Systems

Note: Seating arrangement will be notified before the commencement of the examination by the respective departments.


Vice Principal
(Academics)

Copy to: HODs – CE/EEE/ECE/CSE/BS&H/CoE

Tables of Principal/Vice-Principal (Academics)/Vice-Principal (Administration)